FIG.1

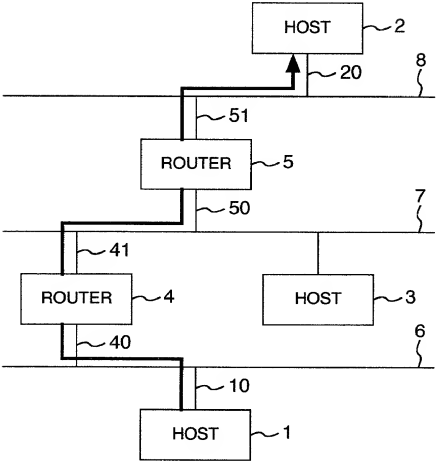

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FIG.2

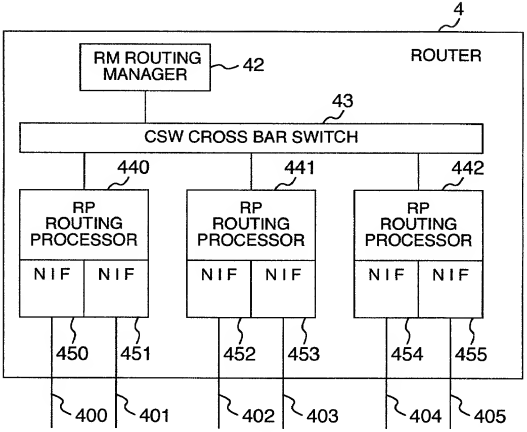

4
ROUTER
RM ROUTING MANAGER
42
43
CSW CROSS BAR SWITCH
440
441
442
RP ROUTING PROCESSOR
RP ROUTING PROCESSOR
RP ROUTING PROCESSOR
NIF
NIF
NIF
NIF
NIF
NIF
450
451
452
453
454
455
400
401
402
403
404
405

FIG.3

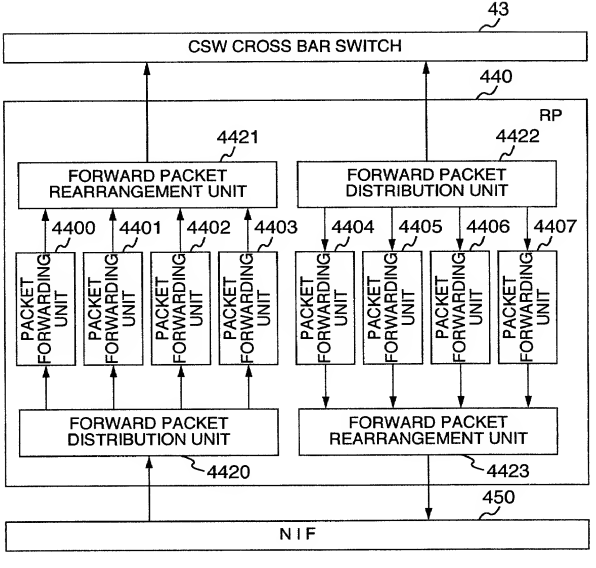
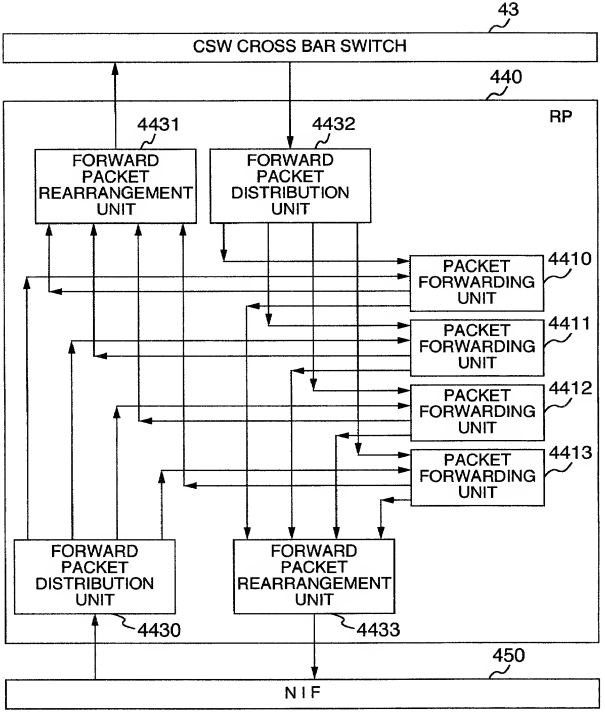

43
CSW CROSS BAR SWITCH
440
RP
4421
4422
FORWARD PACKET REARRANGEMENT UNIT
FORWARD PACKET DISTRIBUTION UNIT
4400
4401
4402
4403
4404
4405
4406
4407
PACKET FORWARDING UNIT
FORWARD PACKET DISTRIBUTION UNIT
4420
FORWARD PACKET REARRANGEMENT UNIT
4423
450
NIF

FIG.4


43
CSW CROSS BAR SWITCH
440
RP
4431
FORWARD PACKET REARRANGEMENT UNIT
4432
FORWARD PACKET DISTRIBUTION UNIT
PACKET FORWARDING UNIT
4410
PACKET FORWARDING UNIT
4411
PACKET FORWARDING UNIT
4412
PACKET FORWARDING UNIT
4413
FORWARD PACKET DISTRIBUTION UNIT
4430
FORWARD PACKET REARRANGEMENT UNIT
4433
450
NIF

TOHZTO' ZOZ79Z60

FIG.5

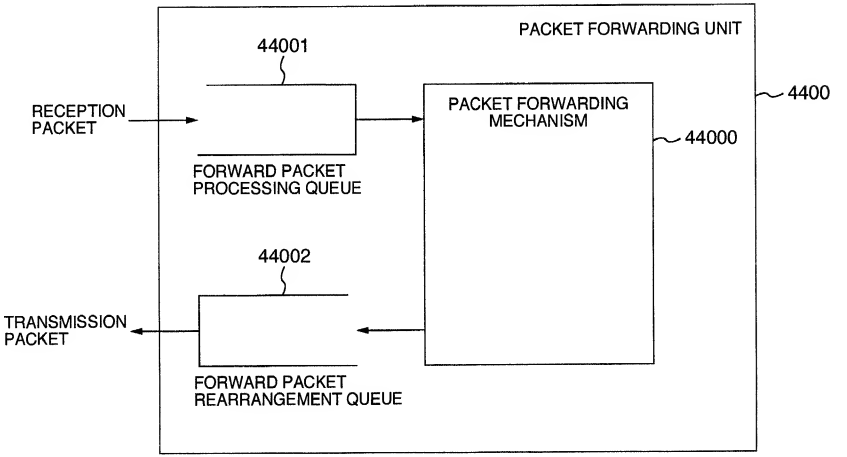


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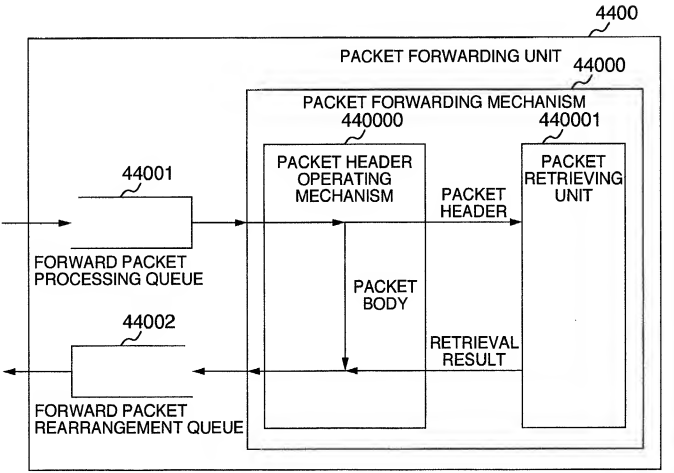
FIG.6



TOHZTO' 20279260

FIG.7

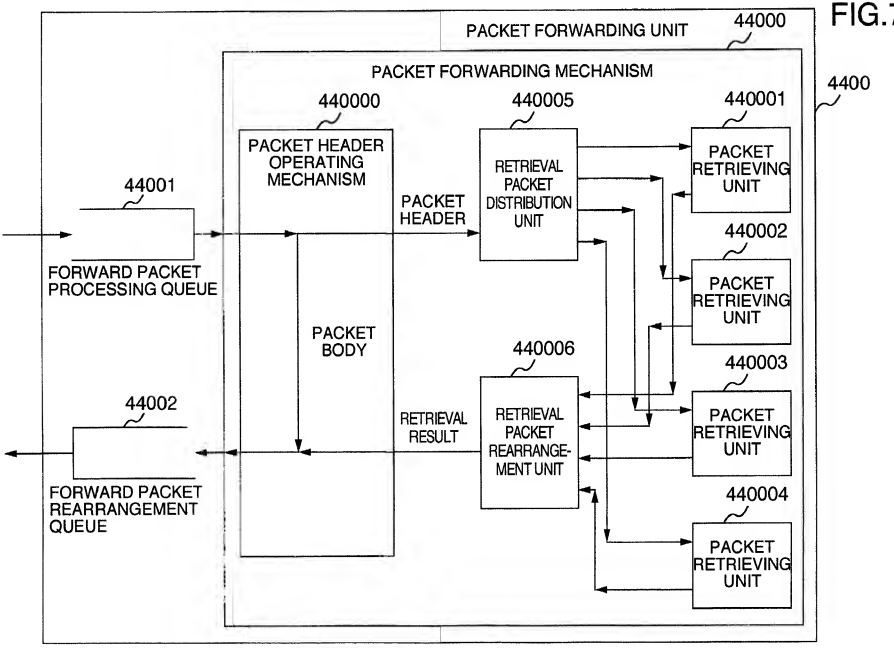


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FIG.8

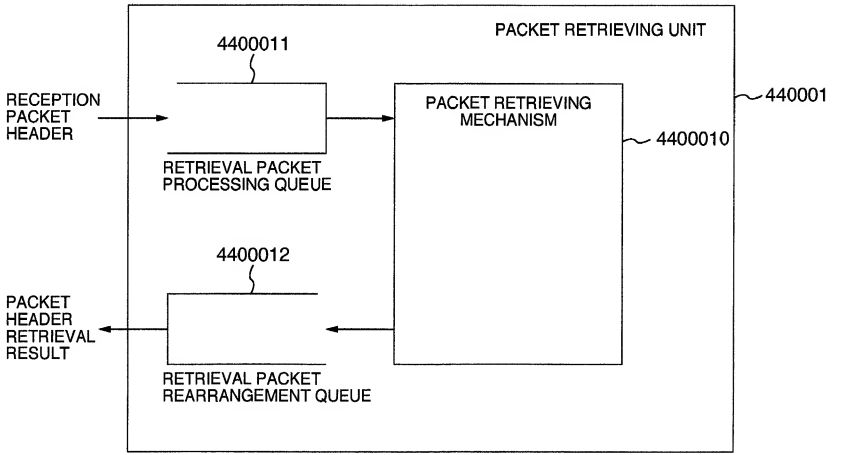

FIG.9

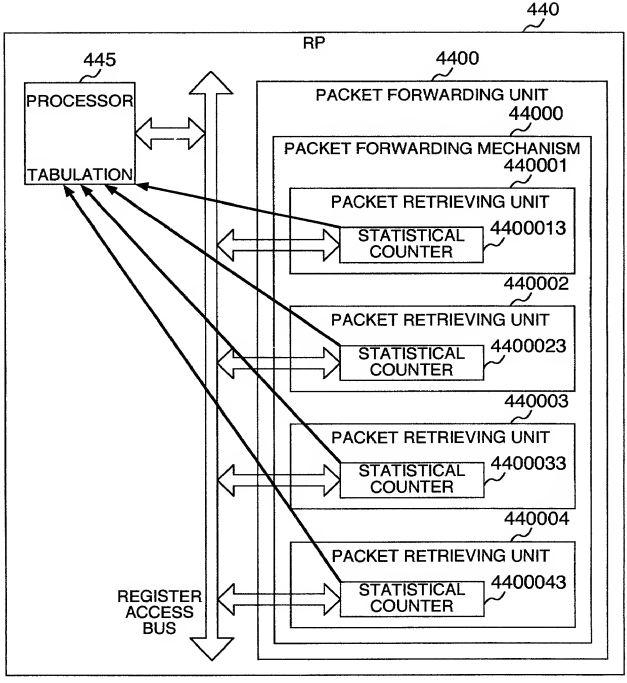
440
RP
445
4400
PROCESSOR
PACKET FORWARDING UNIT
44000
PACKET FORWARDING MECHANISM
440001
TABULATION
PACKET RETRIEVING UNIT
STATISTICAL COUNTER
4400013
440002
PACKET RETRIEVING UNIT
STATISTICAL COUNTER
4400023
440003
PACKET RETRIEVING UNIT
STATISTICAL COUNTER
4400033
440004
PACKET RETRIEVING UNIT
STATISTICAL COUNTER
4400043
REGISTER ACCESS BUS

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FIG.10

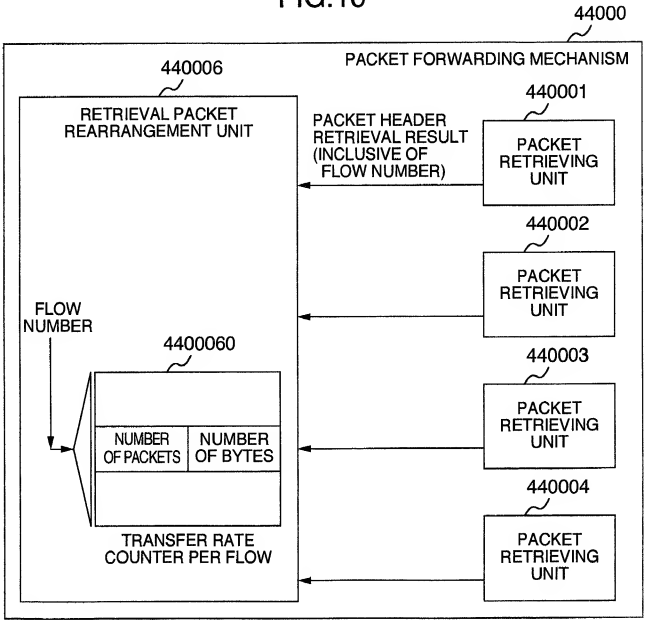

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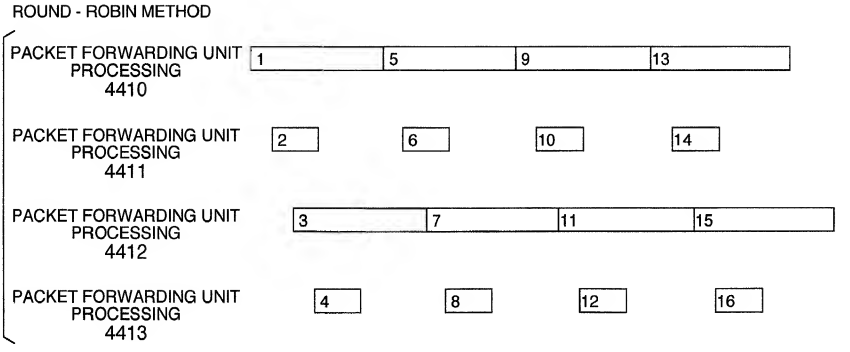
FIG.11



TOHZTO"ZOLL9460

FIG.12

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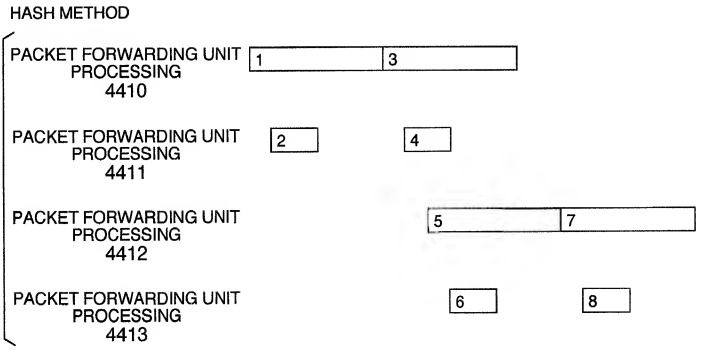
FIG.13



TOKYO 20279260

FIG.14

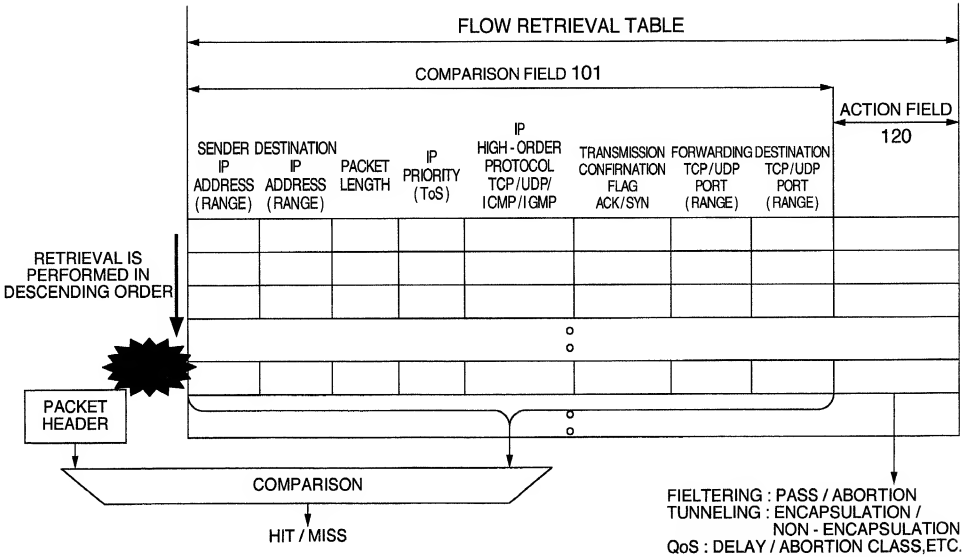


FIG.15

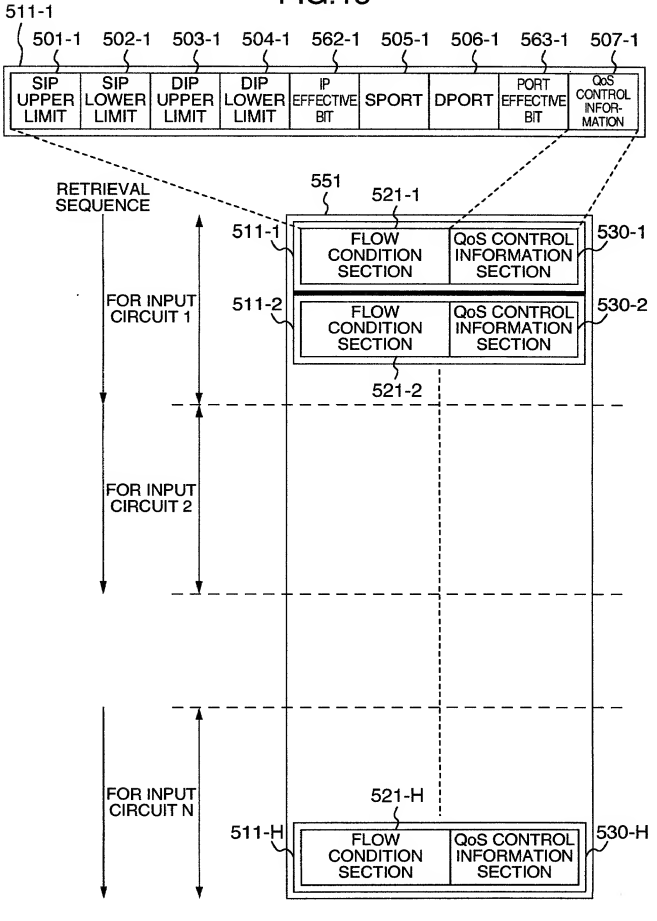


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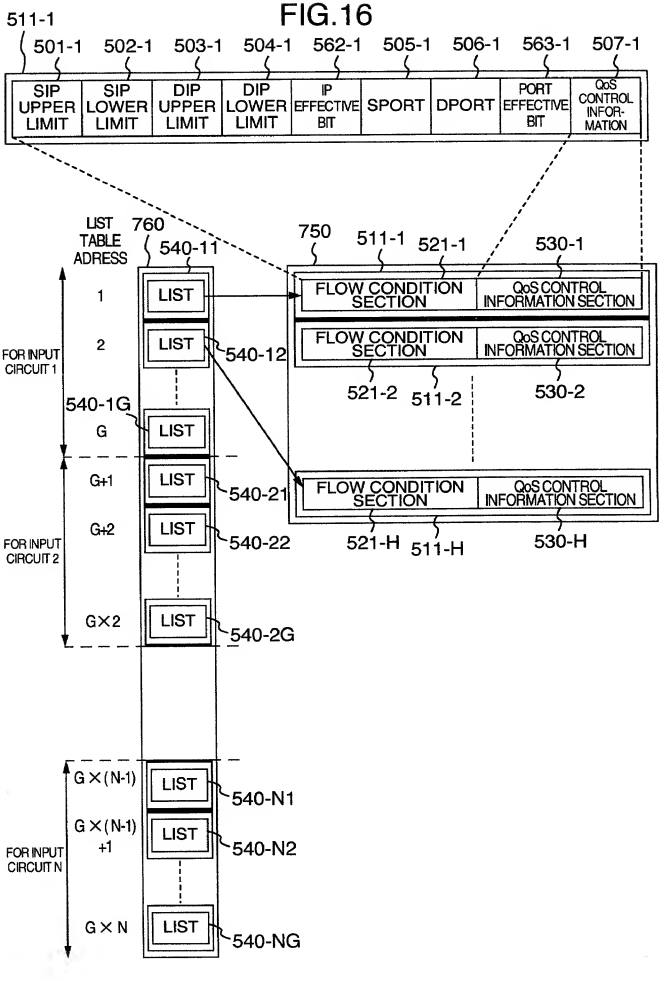
FIG.16


511-1
501-1
502-1
503-1
504-1
562-1
505-1
506-1
563-1
507-1
SIP UPPER LIMIT
SIP LOWER LIMIT
DIP UPPER LIMIT
DIP LOWER LIMIT
IP EFFECTIVE BIT
SPORT
DPORT
PORT EFFECTIVE BIT
QoS CONTROL INFOR-MATION
LIST TABLE ADRESS
760
540-11
750
511-1
521-1
530-1
FLOW CONDITION SECTION
QoS CONTROL INFORMATION SECTION
1
2
G
G+1
G+2
G×2
G×(N-1)
G×(N-1)+1
G×N
LIST
FOR INPUT CIRCUIT 1
FOR INPUT CIRCUIT 2
FOR INPUT CIRCUIT N
540-12
540-1G
540-21
540-22
540-2G
540-N1
540-N2
540-NG
521-2
511-2
530-2
521-H
511-H
530-H